

**MADANAPALLE INSTITUTE OF TECHNOLOGY AND SCIENCE, MADANAPALLE- 517 325**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING – Time Table for 2024-25**

**III B.TECH I SEMESTER - SEC- A**

Room No: EB - 015

Class In Charge: Mr. Veeriah Maddu

With effect from: 22/07/2024

DAY/ TIME	9.10 -10.10	10.10-11.10	11.10 -12.10	12.10-01.00	1.00 – 2.00	2.00 – 3.00	3.00 – 4.00	4.00 – 5.00
MON	AC LAB (B1)/DSP LAB (B2)			<b>B R E A K</b>	EFTL	SS	Holistic Activities	
TUE	UHV	DSP	RM		TT		Holistic Activities	
WED	EFTL	PE-I(BME)	DSP		AC LAB (B2)/DSP LAB (B1)			RM
THU	AC	TT			SOC-III LAB		APT	IPR
FRI	RM	SOC-III	DSP		UHV	IPR	PE-I(BME)	AC
SAT	PE-I(BME)	EFTL	AC		UHV	IPR	Summer Internship - I	

THEORY			CODE	SUBJECT	NAME OF STAFF MEMBER
EFTL			20ECE108	Electromagnetic Fields and Transmission Lines	Dr. Sunku Sreedhar
AC			20ECE109	Analog Communication	Dr. Arifa Ahmed
DSP			20ECE110	Digital Signal Processing	Dr. S. Rajasekaran
OE-I	RM		20IE3M05	Research Methodology	Dr. Rakesh Nath Tiwari
	IPR		20IE3M01	Intellectual Property Rights and Competition Law	Dr. R. Ravindrai
PE-I			20ECE403	Biomedical Electronics	Mrs. Kowsalya P
UHV			20HUM102	Universal Human Values	Dr. Sanoop P
PROJ			20ECE701	Summer Internship - I	Mr. Veeriah Maddu
TT				Technical Training	Mrs. Gutti Nagaswetha (THU)/ Dr. R. Sudhakar (TUE)
APT				Aptitude	Mr. Divjyot Singh Puri
SS				Soft Skills	Dr. S. V. Rasajna
LABORATORY					
AC LAB			20ECE207	Analog Communication Laboratory	Dr. Arifa Ahmed / Mr. Rishikesh Kumar Thakur (MON&WED)
DSP LAB			20ECE208	Digital Signal Processing Laboratory	Mr. Veeriah Maddu (Mon)/ Mrs. V. Sai Anusha (MON) & Mr. G. Charan Kumar (WED)/ Mr. Asif Moiz (WED)
SOC-III			20ECE604	Skill Oriented Course -III Printed Circuit Board (PCB) Designing	Mr. H. Shree Kumar / Mr. G. Subbarao / Mrs. V. Sai Anusha / Mr. Kashiraj Vitthal Kalshetti

Time Table Coordinator

HOD

Principal

**MADANAPALLE INSTITUTE OF TECHNOLOGY AND SCIENCE, MADANAPALLE- 517 325**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING – Time Table for 2024-25**

**III B.TECH I SEMESTER - SEC- B**

**Class In Charge: Mr. M. Kamesh**

**With effect from: 22/07/2024**

**Room No: EB - 014**

DAY/ TIME	9.10 -10.10	10.10-11.10	11.10 -12.10	12.10-01.00	1.00 – 2.00	2.00 – 3.00	3.00 – 4.00	4.00 – 5.00
MON	IPR	SS	RM	<b>B R E A K</b>	TT		Holistic Activities	
TUE	AC LAB (B1)/DSP LAB (B2)				RM	EFTL	Holistic Activities	
WED	PE-I(BME)	DSP	AC		IPR	AC LAB (B2)/DSP LAB (B1)		
THU	UHV	EFTL	AC		RM	APT	SOC-III LAB	
FRI	AC	TT			DSP	UHV	SOC-III	PE-I(BME)
SAT	UHV	EFTL	PE-I (BME)		DSP	IPR	Summer Internship – I	

		CODE	SUBJECT	
EFTL		20ECE108	Electromagnetic Fields and Transmission Lines	Dr. Rakesh Nath Tiwari
AC		20ECE109	Analog Communication	Mr. Veeriah Maddu
DSP		20ECE110	Digital Signal Processing	Dr. Devashish
OE-I	RM	20IE3M05	Research Methodology	Dr. Janmoni Borah
	IPR	20IE3M01	Intellectual Property Rights and Competition Law	Dr. Suman Saurav
PE-I		20ECE403	Biomedical Electronics	Mr. B. Subbarayudu
UHV		20HUM102	Universal Human Values	Mr. B. Subbarayudu
PROJ		20ECE701	Summer Internship - I	Mr. M. Kamesh
TT			Technical Training	Mr. B. Subbarayudu (FRI)/ Mr. Abdul Jaleel D (MON)
APT			Aptitude	Mr. Divjyot Singh Puri
SS			Soft Skills	Dr. S. V. Rasajna
<b>LABORATORY</b>				
AC LAB		20ECE207	Analog Communication Laboratory	Mr. Veeriah Maddu/ Mr. V. S. Prasanth (TUE & WED)
DSP LAB		20ECE208	Digital Signal Processing Laboratory	Dr. Devashish / Mr. Kashiraj Vitthal Kalshetti (TUE) & Mr. H. Shree Kumar (WED)
SOC-III		20ECE604	Skill Oriented Course -III Printed Circuit Board (PCB) Designing	Mr. Veeriah Maddu/Mr. M. Kamesh/ Mr. Chandramouli Joshi /Mr. T. Manivannan

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**III B.TECH I SEMESTER - SEC- C**

Class In Charge: Mr. Rishikesh Kr. Thakur

With effect from: 22/07/2024

Room No: EB - 013

DAY/ TIME	9.10 -10.10	10.10-11.10	11.10 -12.10	12.10-01.00	1.00 – 2.00	2.00 – 3.00	3.00 – 4.00	4.00 – 5.00
MON	AC LAB (B1)/DSP LAB (B2)			<b>B R E A K</b>	RM	PE-I (ADSD)	Holistic Activities	
TUE	RM	APT	SS		TT		Holistic Activities	
WED	PE-I (ADSD)	EFTL	IPR		UHV	AC	DSP	SOC-III
THU	EFTL	TT			AC	RM	<b>SOC-III LAB</b>	
FRI	AC LAB (B2)/DSP LAB (B1)				PE-I (ADSD)	DSP	IPR	UHV
SAT	EFTL	DSP	AC		IPR	UHV	Summer Internship – I	

THEORY		CODE	SUBJECT	NAME OF STAFF MEMBER
EFTL		20ECE108	Electromagnetic Fields and Transmission Lines	Dr. Suman Saurav
AC		20ECE109	Analog Communication	Mrs. Kowsalya P
DSP		20ECE110	Digital Signal Processing	Mr. Chandramouli Joshi
OE-I	RM	20IE3M05	Research Methodology	Dr. Smriti Baruah
	IPR	20IE3M01	Intellectual Property Rights and Competition Law	Mr. G. Charan Kumar
PE-I		20ECE404	Advanced Digital System Design using Verilog HDL	Mr. H. Shree Kumar
UHV		20HUM102	Universal Human Values	Dr. V. Raju
PROJ		20ECE701	Summer Internship - 1	Mr. Rishikesh Kr. Thakur
TT			Technical Training	Dr. Suman Saurav (TUE) / Mr. V. V. S. Trinadh Naidu Kamparapu (THU – 10.10 – 11.10) / Mr. Kiran Palakeeti (THU – 11.10 – 12.10)
APT			Aptitude	Mr. Divjyot Singh Puri
SS			Soft Skills	Dr. S. V. Rasajna
<b>LABORATORY</b>				
AC LAB		20ECE207	Analog Communication Laboratory	Mrs. Kowsalya P/ Ms. S. Kavi Priya (MON & FRI)
DSP LAB		20ECE208	Digital Signal Processing Laboratory	Mr. Chandramouli Joshi/ Mr. Asif Moiz (MON & FRI)
SOC-III		20ECE604	Skill Oriented Course -III Printed Circuit Board (PCB) Designing	Mr. B. Subbarayudu / Dr. G. Reddy Hemantha / Dr. Arifa Ahmed / Dr. Mohit Kumar Singh

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**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING – Time Table for 2024-25**

**III B.TECH I SEMESTER - SEC- D**

**Class In Charge: Mr. B. Subbarayudu**

**With effect from: 22/07/2024**

**Room No: EB - 012**

DAY/ TIME	9.10 -10.10	10.10-11.10	11.10 - 12.10	12.10-01.00	1.00 – 2.00	2.00 – 3.00	3.00 – 4.00	4.00 – 5.00
MON	IPR	AC	APT	<b>B R E A K</b>	DSP	UHV	Holistic Activities	
TUE	SOC-III	TT			DSP	AC	Holistic Activities	
WED	AC LAB (B1)/DSP LAB (B2)				TT		PE-I (ADSD)	IPR
THU	PE-I (ADSD)	AC	SS		RM	IPR	EFTL	UHV
FRI	AC LAB (B2)/DSP LAB (B1)				EFTL	RM	SOC-III LAB	
SAT	RM	PE-I (ADSD)	UHV		DSP	EFTL	Summer Internship – I	

<b>THEORY</b>			
	CODE	SUBJECT	NAME OF STAFF MEMBER
EFTL	20ECE108	Electromagnetic Fields and Transmission Lines	Mr. Rishikesh Kumar Thakur
AC	20ECE109	Analog Communication	Dr. Mohit Kumar Singh
DSP	20ECE110	Digital Signal Processing	Mr. M. Kamesh
OE-I	RM	20IE3M05	Research Methodology
	IPR	20IE3M01	Intellectual Property Rights and Competition Law
PE-I	20ECE404	Advanced Digital System Design using Verilog HDL	Dr. Mohit Kumar Singh
UHV	20HUM102	Universal Human Values	Dr. Agnibha Das Majumdar
PROJ	20ECE701	Summer Internship - 1	Dr. G. Reddy Hemantha
TT		Technical Training	Dr. K. Imran
APT		Aptitude	Mr. B. Subbarayudu
SS		Soft Skills	Dr. C. Kumar (TUE)/ Mr. G. Charan Kumar (WED)
<b>LABORATORY</b>			Mr. Divjyot Singh Puri
AC LAB	20ECE207	Analog Communication Laboratory	Dr. S. V. Rasajna
DSP LAB	20ECE208	Digital Signal Processing Laboratory	Dr. Janmoni Borah / Dr. Smriti Baruah (WED & FRI)
SOC-III	20ECE604	Skill Oriented Course -III Printed Circuit Board (PCB) Designing	Mr. M. Kamesh / Dr. E. Aravindraaj (WED) & Mr. Palachuru Prakash (FRI)



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**III B.TECH I SEMESTER - SEC- E**

Room No: EB - 011

Class In Charge: Mr. G. Mahammed Rafi

With effect from: 22/07/2024

DAY/ TIME	9.10 - 10.10	10.10- 11.10	11.10 - 12.10	12.10-01.00	1.00 – 2.00	2.00 – 3.00	3.00 – 4.00	4.00 – 5.00
MON	UHV	EFTL	PE-I(ES)	<b>B R E A K</b>	RM	AC	Holistic Activities	
TUE	<b>AC LAB (B1)/DSP LAB (B2)</b>				TT		Holistic Activities	
WED	DSP	PE-I(ES)	RM		AC	APT	UHV	EFTL
THU	IPR	TT			DSP	<b>AC LAB (B2)/DSP LAB (B1)</b>		
FRI	EFTL	SS	IPR		PE-I(ES)	UHV	<b>SOC-III LAB</b>	
SAT	AC	IPR	DSP		RM	SOC-III	Summer Internship - I	

THEORY		CODE	SUBJECT	NAME OF STAFF MEMBER
EFTL		20ECE108	Electromagnetic Fields and Transmission Lines	Ms. T. Shanthi
AC		20ECE109	Analog Communication	Mr. Palachuru Prakash
DSP		20ECE110	Digital Signal Processing	Dr. K. Sathesh
OE-I	RM	20IE3M05	Research Methodology	Mr. V. S. Prasanth
	IPR	20IE3M01	Intellectual Property Rights and Competition Law	Ms. T. Shanthi
PE-I		20ECE405	Embedded Systems	Mrs. Gutti Nagaswetha
UHV		20HUM102	Universal Human Values	Dr. V. Raju
PROJ		20ECE701	Summer Internship - I	Mr. G. Mahammed Rafi
TT			Technical Training	Dr. Arifa Ahmed (TUE)/ Mr. R. Sai Venkata Ramana (THU)
APT			Aptitude	Mr. Divjyot Singh Puri
SS			Soft Skills	Dr. S. V. Rasajna
<b>LABORATORY</b>				Mr. Palachuru Prakash/Mrs. Gutti Nagaswetha (TUE & THU)
AC LAB		20ECE207	Analog Communication Laboratory	Mr. V. Nagaraja / Mr. R. Arivarasu (TUE & THU)
DSP LAB		20ECE208	Digital Signal Processing Laboratory	Mrs. Gutti Nagaswetha / Mr. G. Subbarao / Mr. Palachuru Prakash
SOC-III		20ECE604	Skill Oriented Course -III Printed Circuit Board (PCB) Designing	Prakash / Mr. Kashiraj Vitthal Kalshetti

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